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APPLICATION FOR LETTERS PATENT

TITLE: METHOD AND DEVICE TO RETRIEVE RDS
INFORMATION

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Description

1 The present invention relates to the demodulation of radio data system (RDS) signals, in particular to a method to retrieve RDS information according to the pre-characterizing portion of claim 1 and to a RDS demodulator operating according to such a method.

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In fm-broadcasting a stereo-multiplex signal is frequency modulated. The stereo-multiplex signal consists of a stereo-sum signal and a stereo-difference signal. The stereo-difference signal is amplitude modulated with suppressed carrier. To allow a coherent amplitude demodulation of the stereo-difference signal at the receiver, a pilot carrier with half the AM-carrier frequency is added to the stereo-multiplex signal.

The stereo-sum signal and the stereo-difference signal are defined by

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$$m_s(t) = a_l(t) + a_r(t)$$

$$m_d(t) = a_l(t) - a_r(t)$$

wherein $a_l(t)$ is the signal of the left audio channel and $a_r(t)$ is the signal of the right audio channel.

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The stereo-multiplex signal is defined by

$$m_{stmux}(t) = m_s(t) + \sin(2\omega_{pil}t) \cdot m_d(t) + A_{pil} \cdot \sin(\omega_{pil}t)$$

25 wherein ω_{pil} is the carrier frequency and A_{pil} is the amplitude of the carrier.

The RDS-signal is added to the stereo-multiplex signal by modulating the in-phase or in quadrature component of the 3rd harmonic of the pilot carrier. Therefore, the multiplex signal is defined by:

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$$m(t) = m_{stmux}(t) + \cos(3\omega_{pil}t) \cdot m_{rds}(t)$$

wherein $m_{rds}(t)$ is the RDS-signal.

Fig. 8 shows the basic functionality of a stereo-demultiplexer according to the

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$$r(t) = m_s(t) - m_d(t) = (a_l(t) + a_r(t)) - (a_l(t) - a_t(t)) = 2a_r(t)$$

$$l(t) = m_s(t) + m_d(t) = (a_l(t) + a_r(t)) + (a_l(t) - a_r(t)) = 2a_l(t)$$

receives the stereo-sum signal $m_s(t)$ as minuend at its first input and the stereo-difference signal $m_d(t)$ as subtrahend at its second input.

in systems which use the frequency 19 kHz as carrier frequency. Therefore, the 3rd harmonic of the pilot carrier is extracted from the multiplex signal $m(t)$ by filtering said signal with a bandpass filter 33 which has a center frequency of 57 kHz. The output signal of the bandpass filter 33 is input to a COSTAS-loop circuit 34 which locks to the amplitude modulated RDS signal to recover the RDS-carrier, i.e. the 3rd harmonic of the pilot carrier. The recovered RDS-carrier is multiplied with the output signal of the bandpass filter 33 by a second mixer 9 to demodulate the RDS-signal. With this multiplication and a following

1 lowpass filtering by a lowpass filter 10 the coherent amplitude demodulation of
the RDS-signal is performed. The output signal of the lowpass filter 10 is fed
to a RDS-decoder 11 which decodes the RDS-signal $m_{\text{RDS}}(t)$ and outputs the
RDS-data.

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For the detection of a stereo transmitter the stereo-demultiplexer needs to de-
tect the pilot carrier. A reliable method for the detection of the pilot carrier is
to perform a coherent amplitude demodulation of the multiplex signal $m(t)$ with
the pilot carrier. Therefore, the PLL-circuit 1 generates a carrier with a carrier
10 frequency ω_{pil} that is in phase to the pilot carrier. This carrier is mixed with
the multiplex signal by a third mixer 12 and the output signal of the third
mixer 12 $m_g(t)$ is lowpass filtered by a lowpass filter 13 to suppress mixing
products with a higher frequency. The so generated DC-signal $m_h(t)$ is a reli-
able information for the detection of stereo transmission.

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This described stereo-demultiplexer has the particular disadvantage that it
needs a quite high calculation power in a digital solution, especially since a
carrier with the frequency of the 3rd harmonic of the pilot carrier, i.e. 57 kHz,
has to be generated.

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Therefore, it is the object of the present invention to provide a method for ste-
reo-demultiplexing and RDS-demodulation needing less calculation power and
a stereo-demultiplexer including a RDS-demodulator working according to said
method, i.e. needing less calculation power.

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The inventive method is defined in independent claim 1 and the stereo-demul-
tiplexer according to the present invention is defined in independent claim 13.
Preferred embodiments of the inventive method are defined in dependent
claims 2 to 12.

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The present invention and its embodiments will be better understood from the
following detailed description of exemplary embodiments thereof taken in con-
junction with the accompanying drawings, wherein

35 **Fig. 1** shows a stereo-demultiplexer according to a first preferred em-
bodiment of the present invention;

1 **Fig. 2** shows the spectrum of the input signal and the output signal of the first mixer 2 shown in Fig. 1;

5 **Fig. 3** shows the spectrum of the input signal and the output signal of the second mixer 5 shown in Fig. 1;

Fig. 4 shows the spectrum of the input signals and the output signal of the subtractor 6 shown in Fig. 1;

10 **Fig. 5** shows a possible digital realization of a stereo-demultiplexer according to the prior art;

15 **Fig. 6** shows a stereo-demultiplexer according to a second preferred embodiment of the present invention;

Fig. 7 shows a stereo-demultiplexer according to a fourth preferred embodiment of the present invention; and

20 **Fig. 8** shows a stereo-demultiplexer according to the prior art.

25 According to a first aspect of the present invention the coherent amplitude demodulation of the RDS-signal is performed without the generation of an additional carrier with the frequency of the 3rd harmonic of the pilot carrier, i.e. without an additional 57 kHz carrier. Therefore, the present inventions achieves big advantages in terms of calculation power saving.

30 In the following the first aspect of the present invention will be explained on the basis of Fig. 1 to 4 which show a first preferred embodiment according to the present invention. For the sake of simplicity the shown multiplex signal $m(t)$ contains only the signals relevant for the present invention, i.e. the amplitude modulated RDS-signal and the pilot carrier. The demultiplexing and demodulation of the left and right audio signals is exactly performed like in the stereo-demultiplexer according to the prior art shown in Fig. 8. Also, the detection of a stereo transmitter is performed in a similar way. Therefore, the description of these parts of the stereo-demultiplexer according to the present invention which is shown in Fig. 1 is omitted.

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Fig. 3 shows the spectrum $M_s(jf)$ of the stereo-sum signal $m_s(t)$ at the left hand side and the spectrum $M_b(jf)$ of the mirrored pilot carrier at its right hand side.

Fig. 1 shows that the so generated amplitude modulated RDS-signal that has
 35 now a carrier frequency of 19 kHz (a carrier frequency according to the pilot
 carrier) is processed like in the prior art RDS-demodulator shown in Fig. 8.
 The only difference is that the center frequency of the bandpass filter 33

1 shown in Fig. 8 is changed to the frequency of the pilot carrier, i.e. 19 kHz in
the bandpass filter 7 shown in Fig. 1 which receives the output signal of the
subjector 6 and that the carrier frequency of the COSTAS-loop circuit 34
shown in Fig. 8 is changed from the 3rd harmonic of the carrier frequency to
5 the carrier frequency, i.e. from 57 kHz to 19 kHz, in the COSTAS-loop circuit 8
shown in Fig. 1 which receives the output signal of the bandpass filter 7 and
feeds its carrier having the frequency of the pilot carrier to the second mixer 9
that demodulates said output signal of the bandpass filter 7 by multiplying
with the output signal of the COSTAS-loop circuit 8.

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Fig. 5 shows a possible digital solution of the prior art stereo-demultiplexer and
RDS decoder shown in Fig. 8 which elucidates a sampling rate decimation.
Such a sampling rate decimation can be performed, because the frequency
modulated multiplex signal $m(t)$ has a much higher bandwidth than the fre-
quency demodulated and stereo-demultiplexed audio signal and the RDS sig-
15 nal. The combination of the present invention with the digital stereo-demulti-
plexer shown in Fig. 5 has the advantage of a very low calculation power. For a
better understanding, first this digital stereo-demultiplexer is described with-
out the application of the present invention.

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As it is shown in Fig. 5, the stereo-sum signal $m_s(t)$ is generated from the mul-
tiplex signal $m(t)$ by a sampling rate decimation with a decimation factor D by
a first sampling rate decimation filter 14 and a following lowpass filtering with
a lowpass filter 15. The so generated stereo-sum signal $m_s(t)$ is thereafter de-
25 layed by a first delay element with a delay of τ to equalize the group delay of a
second sampling rate decimation filter 21 in the difference path.

Furtheron, a digital PLL circuit 37 (which replaces the PLL circuit 1 of the first
preferred embodiment according to the present invention) is working with the
30 decimated sampling rate. Therefore, the decimation filtering of the sum path,
i.e. the path to generate the stereo-sum signal $m_s(t)$ is used for the sampling
rate decimation of the stereo-sum signal $m_s(t)$ and the pilot carrier, i.e. the
output signal of the first sampling rate decimation filter 14 is not only input to
the lowpass filter 15 but also to the DPLL-circuit 37. This realization is de-
35 scribed in more detail in a parallel european patent application "digital stereo
demultiplexer" of the same applicant.

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- 1 The DPLL-circuit 37 generates a carrier for the coherent amplitude demodulation of the stereo-difference signal $m_d(t)$ and a carrier for the coherent pilot carrier detection, i.e. the detection of a stereo transmitter. Within the DPLL-circuit 37 another carrier is required for synchronization purposes that is in
- 5 quadrature to the pilot carrier. Therefore, the DPLL-circuit 37 generates three carriers, i.e. the 38 kHz carrier for the coherent amplitude demodulation of the stereo-difference signal $m_d(t)$ and a complex 19 kHz carrier. The complex 19 kHz carrier consists of the carrier for the coherent pilot carrier detection and the in quadrature component of this 19 kHz carrier which is required within
- 10 the DPLL-circuit 37 for synchronization purposes.

Similar to the detection of a stereo transmitter described in connection with Fig. 8 the stereo transmitter is detected by a multiplication of the multiplex signal $m(t)$ with the pilot carrier and a following lowpass filtering. This de-

15 modulation is performed by a third mixer 31 which receives the sampling rate decimated multiplex signal from the first sampling rate decimation filter 14 and the pilot carrier generated by the DPLL-circuit 37 on basis of said sampling rate decimated multiplex signal. The output signal of the third mixer 31 is input to a lowpass filter 32 which outputs the pilot detection signal $m_h(t)$.

- 20 The coherent amplitude demodulation of the stereo-difference signal $m_d(t)$ needs to be performed at a sampling rate which is higher than the sampling rate of the audio signal, since the bandwidth of the multiplex signal $m(t)$ is higher than the bandwidth of the audio signal. Therefore, the carrier for the coherent amplitude demodulation of the stereo-difference signal $m_d(t)$ needs to be
- 25 generated with a higher sampling rate. Fig. 5 shows that the carrier is generated with a sampling rate that is D times higher than the sampling rate of the audio signal, since the second harmonic of the pilot carrier generated by the DPLL-circuit 37 is upsampled with a factor of D in a sampling rate upsampling
- 30 circuit 18. To perform the coherent amplitude demodulation which is necessary to generate the stereo-difference signal $m_d(t)$ the so generated carrier with a carrier frequency of $2\omega_{pil}$ that is locked in phase to the pilot carrier is multiplied with the multiplex signal $m(t)$ by a first mixer 20. Furthermore, the output signal of the first mixer 20 is lowpass filtered by a lowpass filter 24 which
- 35 outputs the stereo-difference signal $m_d(t)$. A second sampling rate decimation filter 21 decimates the sampling rate of the output signal of the first mixer 20 by a decimation factor of D by before it is input to the lowpass filter 24. Fur-

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1 passband of the filter. This operation is performed with a fourth mixer 22
 which receives the output signal of the first sampling rate decimation filter 14
 and the sampling rate decimated second harmonic of the pilot carrier which is
 generated by a DPLL-circuit 17 which also receives the output signal of the
 5 first sampling rate decimation filter 14. This sampling rate decimated second
 harmonic of the pilot carrier is also fed to the upsampling circuit 18 before it
 is used for the coherent amplitude demodulation of the stereo difference signal
 $m_d(t)$. The output signal of the second modulator 22, i.e. the mirrored pilot
 carrier, is delayed by a delay of τ in a third delay element 23 to equalize the
 10 group delay of the second sampling rate decimation filter 21 in the difference
 path before it is output as mirrored pilot carrier $m_b(t)$ and therewith as subtra-
 hment to the subtractor 25. The amplitude modulated RDS-signal $m_c(t)$ is input
 to a fourth mixer 26 which downconverts the amplitude modulated RDS signal
 to a complex baseband signal by mixing with the complex 19 kHz carrier. As
 15 mentioned above in connection with Fig. 1, this complex carrier with the fre-
 quency of the pilot tone is already available in the DPLL-circuit 17, since the
 in phase component thereof is needed for the stereo transmitter detection and
 the in quadrature component thereof is needed for synchronization purposes
 within the DPLL-circuit 17.

20 Apart from a further sampling rate decimation by a decimation factor of E in-
 stead of a decimation factor F the following processing of the RDS-signal is
 similar to that of the second preferred embodiment of the present invention
 shown in Fig. 5. The decimation factors D, E and F have the following relation:

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$$D + E = F$$

This digital solution of the second preferred embodiment according to the
 present invention has the advantage that the amplitude modulated RDS-signal
 30 is mixed to the complex baseband in combination with a sampling rate decima-
 tion. Therefore, the respective processing within the COSTAS-loop, the selec-
 tivity filtering and the matched filtering for the optimal decoding of the ampli-
 tude modulated RDS-signal are performed at low sampling rates, as described
 in connection with Fig. 5. As shown in Fig. 6, this can advantageously be
 35 achieved by using the sampling rate decimation filters of the stereo-sum signal
 and the stereo-difference signal for the RDS-demodulation. Additionally, all
 carriers for the frequency shift of the amplitude modulated RDS-signal are

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1 available from the stereo-demultiplexer. Therefore, no additional sin/cos calculations or tables are required at high sampling rates for the coherent demodulation of the amplitude modulated RDS-signal. Therefore, this embodiment has big advantages in terms of calculation power saving.

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As mentioned above, the DPLL has only to generate three carriers, namely the 38 kHz carrier (second harmonic of the pilot carrier) for the coherent amplitude demodulation of the stereo-difference signal and a complex 19 kHz carrier (complex frequency of the pilot carrier). The 19 kHz carrier consists of the carrier for the coherent pilot carrier detection and the in quadrature component of this 19 kHz carrier that is required within the DPLL for synchronization purposes.

According to the second preferred embodiment of the present invention the noise in the band of the pilot carrier is subtracted from the output signal of the second sampling rate decimation filter 21. Therefore, as also shown in connection with the first preferred embodiment of the present invention, the pilot carrier that is included in the stereo-sum signal $m_s(t)$ is mixed with the 38 kHz carrier (second harmonic of the pilot carrier) and delayed to equalize the group delay τ of the sampling rate decimation filters. The so generated signal is subtracted from the output of the sampling rate decimation filter 21 in the stereo-difference path to delete the noise in the band of the pilot carrier.

This generated amplitude modulated RDS-signal with a carrier amplitude of 19 kHz is mixed to the complex baseband by mixing with the complex 19 kHz carrier that is already present within the stereo-demultiplexer. A complex RDS baseband signal $m_{cL}(t)$ is sampling rate decimated by a factor of E before it is fed to the reconstruction lowpass filter 28. This reconstruction lowpass filter 28 is the matched filter for the optimal RDS decoding. The COSTAS-loop circuit 29 locks to the RDS-carrier and generates the amplitude demodulated RDS-signal. The so coherent amplitude demodulated RDS-signal is fed to the RDS-decoder 30 which outputs the RDS data.

Fig. 7 shows the third preferred embodiment according to the present invention. This third preferred embodiment considers that the output signal of the second sampling rate decimation filter 21 can already be used for the RDS-decoding with only a slight performance degradation. The performance degradation

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tion results from the additional noise in the band of the pilot carrier that is added to the RDS-signal. Therefore, the third mixer 26 directly receives the output signal of the second sampling rate decimation filter 21, whereafter the RDS-signal undergoes the same processing as shown and described in connection with Fig. 6. Accordingly, the third preferred embodiment of the present invention shown in Fig. 7 does not comprise the fourth mixer 22, the third delay element 23 and the subtractor 25.

It is obvious that such changes and modifications can also be applied to the first preferred embodiment according to the present invention shown in Fig. 1, i.e. that the bandpass filter 7 directly receives the intermediate signal $m_a(t)$ and the stereo-demultiplexer shown in Fig. 1 would not comprise the third mixer 5 and the subtractor 6.

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